Modeling and Design of High-Efficiency Power Amplifiers Fed by Limited Power Sources

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Agenda





Introduction Proposed PA Modeling **Design Methodology** Study Case Conclusions Acknowledgment **References**



WBANs means?





¹S. Movassaghi, M. Abolhasan, J. Lipman, *et al.*, "Wireless body area networks: A survey", *IEEE Communications Surveys & Tutorials*, vol. 16, no. 3, pp. 1658–1686, 2014.







WBANs means?



• Networks which can be wearable, implanted or around the human body [1].





 Networking at human body level (WBANs + IoT) is expected to cause a dramatic shift in HcS [1].

¹S. Movassaghi, M. Abolhasan, J. Lipman, *et al.*, "Wireless body area networks: A survey", *IEEE Communications Surveys & Tutorials*, vol. 16, no. 3, pp. 1658–1686, 2014.





WPTn concept for implanted device autonomy



WPT node (WPTn) is an autonomous wearable WBAN node used as energy and communication solution for a passive implanted RFID tag that sense biomedical data.







Self-sustaining WPT system power chain



The maximum available power (P_{avs}) of the Implanted power supply is limited by **BOTH** power chain efficiency and P_{avs} of the Power EPS [2].



²A. Fajardo and F. Rangel de Sousa, "Ideal energy power source model and its implications on battery modeling",



Self-sustaining WPT system Design



Traditional design approach:

- The system interactions are reduced to V or I specifications.
- The subsystems are optimized individually.
- Non Traditional design example:
 - Regulator-less PA: A non regulated voltage between the EPS and the power amplifier (PA) was explored in [3].



For Self-sustaining WPT system traditional design approach is inadequate, because maximum η does not necessarily means maximum P_{out} .

³J. C. Rudell, V. Bhagavatula, and W. C. Wesson, "Future integrated sensor radios for long-haul communication", *IEEE Commun. Mag.*, vol. 52, no. 4, pp. 101–109, 2014.





Modeling of the energy-flow process using the PA impedance ports

- At input power ports: R_{DC} and R_{AC} .
- A fraction of the power "dissipated" by PA are transfered to $R_L = R_{lL}$.









Modeling of the energy-flow process using the PA impedance ports

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• The load power depends only on the external elements connected to the PA.





Modeling of the energy-flow process using the PA impedance ports



- At input power ports: R_{DC} and R_{AC} .
- A fraction of the power "dissipated" by PA are transfered to $R_L = R_{IL}$.
- The load power depends only on the external elements connected to the PA.
- The output power port could be modeled by a AC circuit power source.







Circuit power source



A circuit power source imposes the power on its load [4].



⁴R. W. Erickson and D. Maksimovic, *Fundamentals of power electronics*. New York: Springer, 2001.





PA efficiency predicted by the Model I



$$I_{RF} = I_m sin(\omega_0 t)$$

 $V_{RF} = V_m sin(\omega_0 t)$

$$P_{RF} = \frac{I_m^2}{2} R_L = \frac{V_m^2}{2R_L} = \frac{I_m \cdot V_m}{2}$$
$$P_{DC} = I_{DC}^2 R_{DC} = \frac{V_{DC}^2}{R_{DC}} = I_{DC} V_{DC}$$

Output power and efficiency:

$$P_{RF} = PAE \cdot P_{DC} + P_{AC}$$
$$P_{RF} = \eta_D \cdot P_{PA}$$
$$P_{RF} = \eta \cdot (P_{DC} + P_{AC})$$

When P_{AC} is negligible compared to P_{DC} :

$$\eta \approx \textit{PAE} \approx \eta_D \approx \frac{\textit{P}_{\textit{RF}}}{\textit{P}_{\textit{DC}}} = \frac{\textit{R}_L}{2\textit{R}_{\textit{DC}}} \left(\frac{\textit{I}_m}{\textit{I}_{\textit{DC}}}\right)^2$$





PA efficiency predicted by the Model





Class-D PA

DC power= Bias circuit (or the driver circuit) + PA power stage. Therefore, η can be rewritten as:

$$\eta = \frac{1}{2} \frac{R_L R_{DC}}{R_{PA}^2} \left(\frac{I_m}{I_{PA}}\right)^2 = f(G_R)$$

 G_R is the PA impedance factor defined as $G_R = R_{DC}/R_L$, and f(x) is a function dependent on the PA topology.

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⁵B. R. W. Stratakos Anthony J. and S. R. Sanders, "High-efficiency low-voltage dc-dc conversion for portable applications.", in Proc. Int. Workshop on Low-Power Design, Napa, CA. 1994, pp. 21-27.



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 $\langle x(t) \rangle_{T_0} = rac{\omega_0}{2\pi} \int^{t_0 + rac{2\pi}{\omega_0}} x(t) dt$

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Moving average

Considering ideal components, D=50% in the AC-port, high loaded quality factor, the MOSFET (N and P type) as an ideal switch an R_{on} .

$I_m = \frac{V_m}{(R_L + R_{on})} = \frac{4V_{DC}}{\pi (R_L + R_{on})}$ $I_{PA}=\langle i_{PA}(t) angle_{T_0}pprox 2I_m/\pi$ $R_{PA} = \frac{V_{DC}}{I_{PA}} = \frac{\pi^2}{8} \left(R_L + R_{on} \right)$ $R_{bias} = \frac{R_{on}}{f_0 \cdot \alpha \cdot a \cdot b}$





Class-D Modeling Example II





$$\eta = \frac{1}{\left(1 + m\left(G_R\right)\right) \left(1 + k\left(1 + \frac{1}{m(G_R)}\right)\right)}$$

where the function m(x) is given by:

$$h(x) = \frac{1}{2} \left((A+B)x - 1 \pm \sqrt{1 + (2A-2B)x + (A+B)^2 x^2} \right)$$
 if

$$A = f_0 \cdot \alpha \cdot a \cdot b; B = \frac{8}{\pi^2}; k = \frac{A}{B}; G_R = \frac{R_{DC}}{R_L}$$



n





$$G_{opt} = \frac{\sqrt{(k+1)k} + k}{(A+B)\sqrt{(k+1)k} + A(k+1)}$$
(1)

ts maximum value is:

$$\eta_{max} = \frac{\sqrt{k^2 + k}}{\left(k + \sqrt{k^2 + k}\right)\left(k + 1 + \sqrt{k^2 + k}\right)} (2)$$

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- For maximizing the power delivered to the load, the methodology maximize BOTH the power supplied by the harvester and the PA efficiency.
- The methodology uses PA modeling based on its ports and impedance matching concepts.
- The DC-IM implementation could be an DC/DC converter, and the AC-IM implementation could be a L or π network.







St	ep Step Description	Equation
1	Find the P_{avs} of the power EPS and its related variables: optimum load impedance (R_{pavs}), load voltage (V_{pavs}) and current (I_{pavs}).	e.g. for a thermoelectric generator, the internal series resistor of the EPS (R_s) is constant, therefore: $R_{pavs} = R_s$, $V_{pavs} = \frac{V_{DC}}{2}$ and $I_{pavs} = \frac{V_{DC}}{2R_s}$.





	Ste	ep Step Description	Equation
-	1 2	Find the P_{avs} of the power EPS and its related variables: optimum load impedance (R_{pavs}), load voltage (V_{pavs}) and current (I_{pavs}). Fix the voltage in the PA DC-port as the highest for a particular implementation restriction (e.g. V_{max} CMOS process.)	e.g. for a thermoelectric generator, the internal series resistor of the EPS (R_s) is constant, therefore: $R_{pavs} = R_s$, $V_{pavs} = \frac{V_{DC}}{2}$ and $I_{pavs} = \frac{V_{DC}}{2R_s}$. $V_{DC_{opt}} = V_{max}$







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2	Fix the voltage in the PA DC-port as the highest for a particular implementation restriction (e.g. V_{max} CMOS process.)	$V_{DC_{opt}} = V_{max}$
3	Find the DC current that extracts the P_{avs} of the EPS.	$I_{DC_{opt}} = rac{P_{avs}}{V_{max}}$





St	ep Step Description	Equation
1	Find the P_{avs} of the power EPS and its related variables: optimum load impedance (R_{pavs}), load voltage (V_{pavs}) and current (l_{pavs}).	e.g. for a thermoelectric generator, the internal series resistor of the EPS (R_s) is constant, therefore: $R_{pavs} = R_s$, $V_{pavs} = \frac{V_{DC}}{2}$ and $J_{pavs} = \frac{V_{DC}}{2}$.
2	Fix the voltage in the PA DC-port as the highest for a particular implementation restriction (e.g. V_{max} CMOS process.)	$V_{DC_{opt}} = V_{max}$
3	Find the DC current that extracts the P_{avs} of the EPS.	$I_{DC_{opt}} = rac{P_{avs}}{V_{max}}$
4	Find the impedance of the PA DC-port that maximizes the power extracted from the harvester.	$R_{DC_{opt}} = \frac{V_{DC}}{I_{DC}} = \frac{V_{max}^2}{P_{avs}}$





St	ep Step Description	Equation
1	Find the P_{avs} of the power EPS and its related variables: optimum load impedance (R_{pavs}), load voltage (V_{pavs}) and current (I_{pavs})	e.g. for a thermoelectric generator, the internal series resistor of the EPS (R_s) is constant, therefore: $R_{pavs} = R_s$, $V_{pavs} = \frac{V_{DC}}{R_s}$ and $I_{pavs} = \frac{V_{DC}}{R_s}$.
2	Fix the voltage in the PA DC-port as the highest for a particular implementation restriction (e.g. V_{max} CMOS process.)	$V_{DC_{opt}} = V_{max}$
3	Find the DC current that extracts the P_{avs} of the EPS.	$I_{DC_{opt}} = \frac{P_{avs}}{V_{max}}$
4	Find the impedance of the PA DC-port that maximizes the power extracted from the harvester	$R_{DC_{opt}} = rac{V_{DC}}{I_{DC}} = rac{V^2_{max}}{P_{avs}}$
5	Find the optimum load value for maxi- mizing PA efficiency.	$R_{L_{opt}} = G_{R_{opt}} R_{DC_{opt}}$





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St	ep Step Description	Equation
1	Find the P_{avs} of the power EPS and its related variables: optimum load impedance (R_{pavs}), load voltage (V_{pavs}) and current (I_{pavs}).	e.g. for a thermoelectric generator, the internal series resistor of the EPS (R_s) is constant, therefore: $R_{pavs} = R_s$, $V_{pavs} = \frac{V_{DC}}{2}$ and $I_{pavs} = \frac{V_{DC}}{2R_s}$.
2	Fix the voltage in the PA DC-port as the highest for a particular implementation restriction (e.g. V_{max} CMOS process.)	$V_{DC_{opt}} = V_{max}$
3	of the EPS.	$I_{DC_{opt}} = \frac{P_{avs}}{V_{max}}$
4	Find the impedance of the PA DC-port that maximizes the power extracted from the harvester.	$R_{DC_{opt}} = rac{V_{DC}}{I_{DC}} = rac{V^2_{max}}{P_{avs}}$
5	Find the optimum load value for maxi- mizing PA efficiency.	$R_{L_{opt}} = G_{R_{opt}} R_{DC_{opt}}$
6	AC impedance matching networks (DC-IM and AC-IM).	$M = \sqrt{\frac{R_{DC_{opt}}}{R_{P_{avs}}}} = \sqrt{\frac{V^2_{max}}{P_{avs} \cdot R_{P_{avs}}}}$



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St	ep Step Description	Equation
1	Find the P_{avs} of the power EPS and its related variables: optimum load impedance (R_{pavs}), load voltage (V_{pavs}) and current (I_{pavs}).	e.g. for a thermoelectric generator, the internal series resistor of the EPS (R_s) is constant, therefore: $R_{pavs} = R_s$, $V_{pavs} = \frac{V_{DC}}{2}$ and $I_{pavs} = \frac{V_{DC}}{2R_s}$.
2	Fix the voltage in the PA DC-port as the highest for a particular implementation	$V_{DC_{opt}} = V_{\max}$
3	Find the DC current that extracts the P_{avs} of the EPS.	$I_{DC_{opt}} = \frac{P_{avs}}{V_{max}}$
4	Find the impedance of the PA DC-port that maximizes the power extracted from the barvester	$R_{DC_{opt}} = rac{V_{DC}}{I_{DC}} = rac{V^2_{max}}{P_{avs}}$
5	Find the optimum load value for maximizing PA efficiency.	$R_{L_{opt}} = G_{R_{opt}} R_{DC_{opt}}$
6	Find the specifications of the DC and AC impedance matching networks (DC-IM and AC-IM).	$M = \sqrt{\frac{R_{DC_{opt}}}{R_{P_{avs}}}} = \sqrt{\frac{V_{max}^2}{P_{avs} \cdot R_{P_{avs}}}}$
	····,·	$n = \sqrt{\frac{R_L}{R_{L_{opt}}}} = \sqrt{\frac{P_{avs} \cdot R_L}{G_{R_{opt}} V^2_{max}}}$

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Class A Study Case - PA Modeling I





$$\eta = \begin{cases} 0.5 \cdot G_R & G_R < 1 ; \Rightarrow I_m = I_{PA} \\ \frac{1}{2G_R} & G_R \ge 1 ; \Rightarrow V_m = V_C \end{cases}$$

Where,
$$G_R = \frac{R_{DC}}{R_L}$$
.

Ideal class A PA

This efficiency is maximum when:

$$G_{R_{opt}} = 1$$

its maximum value is:

$$\eta_{max} = 50\%$$



Class A Study Case - PA Modeling II



As a proof of concept a class A PA was designed, simulated and implemented.

I _{PA}	f ₀	R _L
1mA	100 kHz	1kΩ

The simulation setup uses the harmonic balance simulation technique in the Advanced Design System (ADS[®]) software. V_{AC} and R_{PA} sweeps were implemented.

\cap		PA	simula	tion r	esults		1	\frown	
R _P	Limit	Vac 1	P _{DC} w/oIC	P _{DC}	P _L w/o l	PL WI	η w/o I	η w I	
()	.000	φt	(mW)	(mW)	(mW)	(mW)	(%)	(%)	
0.5	$V_m = V_C$	0.530	0.564	0.564	0.138	0.138	24.5	24.5	
1.0	$V_m = V_c$	1.181	1.127	1.127	0.545	0.54	48.3	48.2	
1.5	Im=IPA	1.180	1.691	1.691	0.545	0.545	32.2	32.1	
2.0	I _m =I _{PA}	1.180	2.254	2.254	0.545	0.545	24.2	24.1	1
							~		



The R_{PA} sweep was implemented by a fixed current (I_{PA} =1mA) and a V_{DC} sweep. The circuit was simulated with and without the output LC tank filter (Only C_0).

Class A Study Case - PA Modeling III



In the experimental setup the I_{PA} was fixed to 1 mA and the R_{PA} was set with the V_{DC} . In this setup, V_{AC} was incremented until the PA operates at the limit of the class-A operation ($I_m = I_{PA}$ or $V_m = V_C$).

	\sim	ς,	PA expe	erimental r	esults		\frown
(R _{PA} (Ω)	Limit type	$ v_{ac} _{\phi_t}$	LC tank	P _{DC} (mW)	<i>P_L</i> (mW)	η (%)
	0.50 1.00 1.50 2.00	V _m =V _C V _m =V _C I _m =I _{PA} I _m =I _{PA}	0.679 1.516 1.516 1.516	w/o w/o w/o w/o	0.5002 10.003 15.002 2.002	0.1242 0.5004 0.5010 0.5010	24.8 50 33.3 25
	$\mathbf{\mathcal{I}}$						∇





Class A Study Case - PA Modeling IV



The predicted efficiency by the proposed PA model and the results (simulated and experimental) are plotted in the Figure



Class A Study Case - Proposed Methodology (PA + Power EPS)



In order to verify experimentally the proposed methodology without the practical limitations of the commercial harvesters and the impedance matching networks, we choose a scenario with the following specifications: a emulated power EPS with $P_{avs} = 1$ mW and $R_{pavs} = 1$ k Ω , a resistive load of $R_L = 1$ k Ω , and $f_0 = 100$ kHz.

Metodology Results						
$V_{DC_{opt}}$	$I_{DC_{opt}}$	$R_{DC_{opt}}$	$R_{L_{opt}}$	М	n	
1V	1mA	1 k Ω	1kΩ	1	1	





• A design methodology for a generic PA fed by a power EPS was proposed.







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- A design methodology for a generic PA fed by a power EPS was proposed.
- As a proof of concept a class-A PA was designed, implemented and tested.
- The results reflect that the designed PA extracts the maximum available power of the source with its maximum efficiency.
- For maximizing the power on the load in a system powered by Power EPSs, the traditional approach based only the system efficiency is inadequate. Furthermore, the designing of EPSs, Energy converters (i.e. PAs) and circuits that could take advantage of the use of power specifications instead of predefined voltage or current condition is a open challenge.





Acknowledgment





The first author would like to thank COLCIENCIAS and the Pontificia Universidad Javeriana for the financial support. Also, the authors would like to thank the CNPq and the INCT/NAMITEC for their partial financial support and all the students of the Radio Frequency integrated circuits Group (GRF-UFSC) for the important discussions.





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